Case4:10-cv-05014-LB Document1 Filed11/05/10 Page1 of 10 E-FILINGADR SIMPSON THACHER & BARTLETT LLP 1 GEORGE M. NEWCOMBE (202898) ORIGINAL FILED gnewcombe@stblaw.com 2 JEFFREY E. OSTROW (213118) jostrow@stblaw.com 3 HARRISON J. FRAHN IV (206822) NOV 0 5 2010 hfrahn@stblaw.com 4 Richard W. Wieking Clerk, U.S. District Court Northern District of California PATRICK E. KING (211975) pking@stblaw.com 5 2550 Hanover Street Palo Alto, California 94304 6 Telephone: (650) 251-5000 Facsimile: (650) 251-5002 7 Attorneys for Plaintiffs 8 ELPIDA MEMORY, INC. and ELPIDA MEMORY (USA) INC. 9 UNITED STATES DISTRICT COURT 10 NORTHERN DISTRICT OF CALIFORNIA 11 12  $\sqrt{10-05014}$ ELPIDA MEMORY, INC., and 13 ELPIDA MEMORY (USA) INC., COMPLAINT FOR PATENT 14 INFRINGEMENT Plaintiffs, 15 DEMAND FOR JURY TRIAL V. 16 ON SEMICONDUCTOR CORPORATION, and 17 SEMICONDUCTOR COMPONENTS INDUSTRIES, L.L.C., 18 Defendants. 19 20 Plaintiffs ELPIDA MEMORY, INC., and ELPIDA MEMORY (USA) INC. 21 (collectively, "Elpida"), by and through their undersigned counsel, hereby allege as follows: 22 **PARTIES** 23 Elpida Memory, Inc. is a corporation organized under the laws of Japan 1. 24 with its principal place of business at Sumitomo Seimei Yaesu Building, 3rd Floor, 2-1 Yaesu 2-25 chome Chuo-ku, Tokyo, Japan. Elpida Memory, Inc. does business in the Northern District of 26 California. 27 28 COMPLAINT FOR PATENT INFRINGEMENT DEMAND FOR JURY TRIAL

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#### INTRADISTRICT ASSIGNMENT

9. This is an Intellectual Property Action to be assigned on a district-wide basis pursuant to Civil Local Rule 3-2(c).

#### BACKGROUND

- 10. Elpida is a leading manufacturer of Dynamic Random Access Memory ("DRAM") integrated circuits. Elpida's design, manufacturing and sales operations are backed by world class technological expertise. Elpida utilizes the most advanced manufacturing technologies available, and its products feature such characteristics as high-density, high-speed, low power and small packaging profiles. Elpida provides DRAM solutions across a wide range of applications, including personal computers, servers, mobile devices and digital consumer electronics.
- 11. Elpida (and its predecessor) sought and obtained patent protection pertaining to many inventions covering leading innovations in DRAM technology. The inventions protected by Elpida's patents resulted from the investments of large monetary sums in research and development.
- 12. On January 17, 1995, the United States Patent and Trademark Office duly and legally issued U.S. Patent No. 5,383,080, entitled "Semiconductor Integrated Circuit Having Voltage Limiter Circuit" ("the '080 Patent"). A true and correct copy of the '080 Patent is attached hereto as Exhibit A.
- 13. Elpida is the owner by assignment of the '080 Patent and has the exclusive right to license the '080 Patent as well as to sue for and collect fees, costs and damages, including damages for past infringement of the '080 Patent.
  - 14. The '080 Patent generally relates to semiconductor devices.
- 15. Upon information and belief, Defendants are a global supplier of power, analog, digital signal processing, mixed signal, advanced logic, data management semiconductors, memory and standard semiconductor components and integrated circuits. Defendants design, manufacture and market an extensive portfolio of semiconductor components. Specifically, Defendants design, manufacture and market the following products: the CAT4201 and the NCP3102B (the "Exemplar Products").

- 16. Upon information and belief, Defendants have manufactured, used, caused to be used, offered to sell and/or sold their products, including but not limited to the Exemplar Products, in the Northern District of California and elsewhere in the United States.
- Upon information and belief, Defendants have known of the '080 Patent at 17. least since April 2, 2010 when it became the subject of publicly filed litigation in a federal district court concerning technology similar to that which is embodied in Defendants' products.

#### **COUNT I**

#### (Infringement of the '080 Patent)

- 18. Elpida hereby restates and realleges the allegations set forth in paragraphs 1-17 above and incorporates them by reference, as though fully set forth herein.
- 19. Elpida is informed and believes, and on that basis alleges, that Defendants have infringed and are infringing the '080 Patent, have contributed and are contributing to infringement of the '080 Patent, and/or have actively induced and are actively inducing others to infringe the '080 Patent, by committing acts defined in 35 U.S.C. § 271 as unlawful and infringing, including but not limited to making, using, offering for sale, selling and/or importing products that infringe one or more claims of the '080 Patent. Defendants' infringing products include, but are not limited to, the Exemplar Products. All such acts by Defendants have been without authority or license from Elpida.
- 20. As a consequence of Defendants' infringing activities, Elpida has been damaged in an amount not yet determined. Defendants' infringement of Elpida's exclusive rights under the '080 Patent will continue to damage Elpida, causing irreparable harm, for which there is no adequate remedy at law, unless Defendants are enjoined by this Court.
- 21. Upon information and belief, Defendants' infringement is willful and deliberate, entitling Elpida to increased damages under 35 U.S.C. § 284 and to attorneys' fees and costs incurred in prosecuting this action under 35 U.S.C. § 285.

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1	Dated: November 5, 2010	Respectfully submitted,
2		By MI
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# **EXHIBIT A**



US005383080A

## United States Patent [19]

Etoh et al.

Patent Number: [11]

5,383,080

Date of Patent: [45]

Jan. 17, 1995

[54]	SEMICONDUCTOR	INTEGRATED	CIRCUIT
	HAVING VOLTAGE		

[75] Inventors: Jun Etoh, Hachioji; Masakazu Aoki,

Tokorozawa: Masashi Horiguchi, Kawasaki; Shigeki Ueda, Hachioji; Hitoshi Tanaka, Tachikawa; Kazuhiko Kajigaya, Iruma; Tsugio

Takahashi, Ome; Hiroshi Kawamoto,

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[73] Assignees: Hitachi, Ltd.; Hitachi VLSI Engineering Corporation, both of

Tokyo, Japan

[21] Appl. No.: 917,995

[22] Filed: Jul. 24, 1992

Foreign Application Priority Data [30] Jul. 25, 1991 [JP] Japan ...... 3-186100

[51] Int. Cl.6 ...... H02H 3/00 U.S. Cl. ...... 361/56; 361/91

[58] Field of Search ....... 361/56, 91, 118; 363/20, 21, 97; 365/226, 227 [56] References Cited

U.S. PATENT DOCUMENTS

4,731,719 3/1988 Neison ...... 363/20 4,930,112 5/1990 Tanaka et al. ...... 365/226

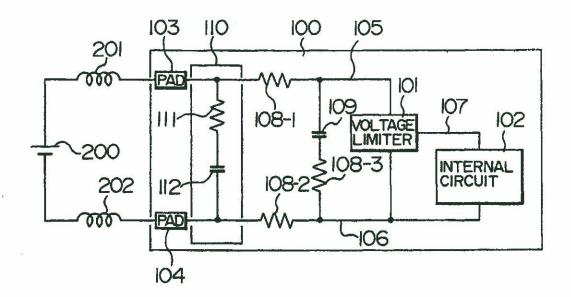
Primary Examiner-Marc S. Hoff Assistant Examiner-S. Jackson

Attorney, Agent, or Firm-Antonelli, Terry, Stout & Kraus

ABSTRACT

A voltage limiter circuit is disposed in a semiconductor IC chip in order to reduce an operating voltage of an internal circuit of a scaled-down element. A small capacitance of a Vcc wiring by the disposition constitutes a resonance circuit together with an inductance of the Vcc wiring. Resonance at the resonance circuit causes large variation of a supply current and noise. An additional capacitance is connected between the Vcc wiring and a Vss wiring in order to suppress the variation and noise. The capacitance is formed by a PN junction and is connected in series to a damping resistance.

25 Claims, 7 Drawing Sheets



U.S. Patent

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FIG. 1

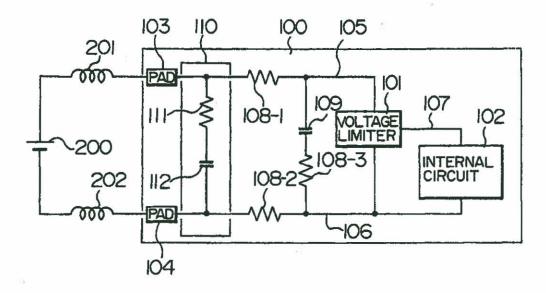
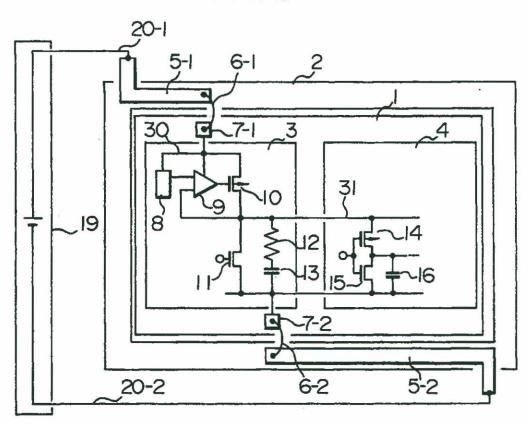


FIG. 2



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FIG. 3A

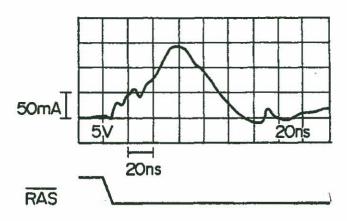


FIG. 3B

