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6 7	Attorney for Plaintiff,							
8	DRAM MEMORY TECHNOLOGIES I	LLC						
9	LINITED STATES	DISTRICT COURT						
	UNITED STATES DISTRICT COURT							
10	CENTRAL DISTRIC	CT OF CALIFORNIA.						
11	DRAM MEMORY TECHNOLOGIES LLC,	SAGU11-0033200 (SSX)						
12	Plaintiff,	ORIGINAL COMPLAINT FOR						
13	·	PATENT INFRINGEMENT						
14	VS.							
15		HIDS/TOLAL DEMANDED						
16	ELITE SEMICONDUCTOR MEMORY TECHNOLOGY INC.;	JURY TRIAL DEMANDED						
17	ETRON TECHNOLOGY AMERICA,							
18	INC.;	Filing Date: TBD						
	ETRON TECHNOLOGY, INC.; AND	Trial Date: TBD						
19	INTEGRATED SILICON SOLUTION,	Discovery Cut-off: TBD  Motion Cut-off: TBD						
20	INC. Defendants.	Widtion Cut-on. 1DD						
21	Bolondaries.							
22								
23	This is an action for patent infringer	nent in which DRAM Memory						
24	Technologies LLC submits this Complaint	against Elite Semiconductor Memory						
25								
26	Technology Inc.; Etron Technology Ameri	ica, Inc.; Etron Technology, Inc.; and						
27	Integrated Silicon Solution, Inc. (collective	ely "Defendants").						
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	1.7							

#### **PARTIES**

- DRAM Memory Technologies LLC ("DRAM MEMTECH" or "Plaintiff") is a California limited liability company with a place of business at 500 Newport Center Drive, Suite 700, Newport Beach, CA 92660.
- 2. On information and belief, Elite Semiconductor Memory Technology Inc. ("ESMT") is an entity organized under the laws of Taiwan with a place of business at No. 23, Industry E. Road IV Science-Based Industrial Park, Hsinchu 300, Taiwan.
- 3. On information and belief, Etron Technology America, Inc. ("Etron America") is a California corporation with a place of business at 3375 Scott Blvd. Suite 128, Santa Clara, California 95054.
- 4. On information and belief, Etron Technology, Inc. ("Etron Taiwan") is an entity organized under the laws of Taiwan with a place of business at No. 6

  Technology Rd. 5, Science-Based Industrial Park, Hsinchu, 30078, Taiwan. Etron

  America and Etron Taiwan are referred to collectively as "Etron."
- 5. On information and belief, Integrated Silicon Solution, Inc. ("ISSI") is a Delaware corporation with a place of business at 1940 Zanker Road, San Jose, CA 95112-4216.

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#### **JURISDICTION AND VENUE**

- 6. This action arises under the patent laws of the United States, Title 35 of the United States Code. This Court has subject matter jurisdiction pursuant to 28 U.S.C. §§ 1331 and 1338(a). On information and belief, Defendants are subject to this Court's specific and general personal jurisdiction, pursuant to due process and/or the California Long Arm Statute, due at least to their substantial business in this forum, including at least a portion of the infringements alleged herein. On information and belief, within this district Defendants, directly and/or through intermediaries, have advertised (including through websites), offered to sell, sold and/or distributed infringing products, and/or have induced the sale and use of infringing products. Further, on information and belief, Defendants are subject to the Court's general jurisdiction, including from regularly doing or soliciting business, engaging in other persistent courses of conduct, and/or deriving substantial revenue from goods and services provided to individuals in California.
- 7. Venue is proper in this district under 28 U.S.C. §§ 1391(b), 1391(c) and 1400(b). On information and belief, from and within this Judicial District each Defendant has committed at least a portion of the infringements at issue in this case. Without limitation, on information and belief, within this district Defendants, directly and/or through intermediaries, have advertised (including through websites), offered

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to sell, sold and/or distributed infringing products, and/or have induced the sale and use of infringing products.

#### **COUNT I**

### **INFRINGEMENT OF U.S. PATENT NO. 6,765,838**

- 8. United States Patent No. 6,765,838 ("the '838 patent"), entitled "Refresh Control Circuitry for Refreshing Storage Data," issued on July 20, 2004.
- 9. DRAM MEMTECH is the present assignee of the entire right, title and interest in and to the '838 patent, including all rights to sue for past and present infringement. Accordingly, DRAM MEMTECH has standing to bring this lawsuit for infringement of the '838 patent.
- 10. The various claims of the '838 patent cover, inter alia, semiconductor memory devices, namely dynamic random access memory (hereinafter "DRAM") chips, comprising refresh address generating circuitry for generating a multi-bit refresh address designating a memory cell to be refreshed, and refresh activating circuitry for generating a refresh array activating signal for activating a refresh operation in accordance with a specific address bit of the refresh address and a refresh request.
- 11. On information and belief, ESMT has been and now is infringing the '838 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips,

comprising refresh address generating circuitry for generating a multi-bit refresh address designating a memory cell to be refreshed, and refresh activating circuitry for generating a refresh array activating signal for activating a refresh operation in accordance with a specific address bit of the refresh address and a refresh request, including, but not limited to, ESMT's M53D128168A Mobile DDR SDRAM.

- 12. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, ESMT is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '838 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ESMT is and/or has been inducing users to use said chips.
- 13. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, ESMT is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '838 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 14. Accordingly, on information and belief ESMT is liable for infringement of the '838 patent.

- 15. On information and belief, Etron has been and now is infringing the '838 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips, comprising refresh address generating circuitry for generating a multi-bit refresh address designating a memory cell to be refreshed, and refresh activating circuitry for generating a refresh array activating signal for activating a refresh operation in accordance with a specific address bit of the refresh address and a refresh request, including but not limited to Etron's EM68916CWQA DDRII SDRAM.
- 16. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, Etron is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '838 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further Etron is and/or has been inducing users to use said chips.
- 17. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, Etron is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made

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or especially adapted for use in an infringement of the '838 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.

- 18. Accordingly, on information and belief Etron is liable for infringement of the '838 patent.
- 19. On information and belief, ISSI has been and now is infringing the '838 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips, comprising refresh address generating circuitry for generating a multi-bit refresh address designating a memory cell to be refreshed, and refresh activating circuitry for generating a refresh array activating signal for activating a refresh operation in accordance with a specific address bit of the refresh address and a refresh request, including but not limited to, ISSI's IS43DR83200A, IS43/46DR16160A, and IS43DR32160A DDR2 SDRAMs.
- 20. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, ISSI is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '838 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ISSI is and/or has been inducing users to use said chips.

- 21. On information and belief, at least since receiving notice of the '838 patent, including at a minimum from this lawsuit, ISSI is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '838 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 22. Accordingly, on information and belief ISSI is liable for infringement of the '838 patent.

#### **COUNT II**

## **INFRINGEMENT OF U.S. PATENT NO. 5,703,831**

- 23. United States Patent No. 5,703,831 ("the '831 patent"), entitled "Synchronous Semiconductor Memory Device Having Internal Circuitry Enabled Only When Commands Are Applied In Normal Sequence," issued on December 30, 1997.
- 24. DRAM MEMTECH is the present assignee of the entire right, title and interest in and to the '831 patent, including all rights to sue for past and present infringement. Accordingly, DRAM MEMTECH has standing to bring this lawsuit for infringement of the '831 patent.

- 25. The various claims of the '831 patent cover, inter alia, DRAM chips operating in synchronization with an externally applied clock signal, comprising a first command decoder generating an activating signal activating a predetermined first internal operation, and a second command decoder enabled in response to the activating signal from said first command decoder to activate a second internal operation.
- 26. On information and belief, ESMT has been and now is infringing the '831 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips operating in synchronization with an externally applied clock signal, comprising a first command decoder generating an activating signal activating a predetermined first internal operation, and a second command decoder enabled in response to the activating signal from said first command decoder to activate a second internal operation, including but not limited to ESMT's M14D5121632A DDR II SDRAM and M13S256328A DDR SDRAM.
- 27. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, ESMT is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '831 patent,

including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ESMT is and/or has been inducing users to use said chips.

- 28. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, ESMT is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '831 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 29. Accordingly, on information and belief ESMT is liable for infringement of the '831 patent.
- 30. On information and belief, Etron has been and now is infringing the '831 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips operating in synchronization with an externally applied clock signal, comprising a first command decoder generating an activating signal activating a predetermined first internal operation, and a second command decoder enabled in response to the activating signal from said first command decoder to activate a second internal operation, including but not limited to, Etron's EM68916CWQA DDR II SDRAM and EM6AB160TSA DDR SDRAM.

- 31. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, Etron is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '831 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further Etron is and/or has been inducing users to use said chips.
- 32. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, Etron is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '831 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 33. Accordingly, on information and belief Etron is liable for infringement of the '831 patent.
- 34. On information and belief, ISSI has been and now is infringing the '831 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely ISSI chips operating in synchronization with an externally applied clock signal, comprising a first command decoder generating an activating signal activating a predetermined first internal

operation, and a second command decoder enabled in response to the activating signal from said first command decoder to activate a second internal operation, including without limitation, ISSI's IS43DR86400B DDR2 SDRAM and IS43/46LR16800E Mobile DDR SDRAM.

- 35. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, ISSI is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '831 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ISSI is and/or has been inducing users to use said chips.
- 36. On information and belief, at least since receiving notice of the '831 patent, including at a minimum from this lawsuit, ISSI is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '831 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 37. Accordingly, on information and belief ISSI is liable for infringement of the '831 patent.

### **COUNT III**

#### INFRINGEMENT OF U.S. PATENT NO. 6,396,747

- 38. United States Patent No. 6,396,747 ("the '747 patent"), entitled "Semiconductor Memory Device Capable of High Speed Input/Output of Wide Bandwidth Data By Improving Usage Efficiency of External Data Bus," issued on May 28, 2002.
- 39. DRAM MEMTECH is the present assignee of the entire right, title and interest in and to the '747 patent, including all rights to sue for past and present infringement. Accordingly, DRAM MEMTECH has standing to bring this lawsuit for infringement of the '747 patent.
- 40. The various claims of the '747 patent cover, inter alia, DRAM chips capable of inputting/outputting data signals transmitted as a data train in time series by an external data bus in each of one read access operation and one write access operation.
- 41. On information and belief, ESMT has been and now is infringing the '747 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips capable of inputting/outputting data signals transmitted as a data train in time series by an external data bus in each of one read access operation and one write access operation, including without limitation, ESMT's M14D5121632A DDR II SDRAM and M13S256328A DDR SDRAM.

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- 42. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, ESMT is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '747 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ESMT is and/or has been inducing users to use said chips.
- 43. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, ESMT is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '747 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- Accordingly, on information and belief ESMT is liable for infringement 44. of the '747 patent.
- 45. On information and belief, Etron has been and now is infringing the '747 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips capable of inputting/outputting data signals transmitted as a data train in time series by an external data bus in each of one read access operation and one write access operation,

including, without limitation, Etron's EM68916CWQA DDR II SDRAM and EM6AB160TSA DDR SDRAM.

- 46. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, Etron is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '747 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further Etron is and/or has been inducing users to use said chips.
- 47. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, Etron is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '747 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 48. Accordingly, on information and belief Etron is liable for infringement of the '747 patent.
- 49. On information and belief, ISSI has been and now is infringing the '747 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips capable of

inputting/outputting data signals transmitted as a data train in time series by an external data bus in each of one read access operation and one write access operation, including without limitation, ISSI's IS43DR86400B DDR2 SDRAM and IS43/46LR16800E Mobile DDR SDRAM.

- 50. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, ISSI is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '747 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ISSI is and/or has been inducing users to use said chips.
- 51. On information and belief, at least since receiving notice of the '747 patent, including at a minimum from this lawsuit, ISSI is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '747 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 52. Accordingly, on information and belief ISSI is liable for infringement of the '747 patent.

### **COUNT IV**

#### **INFRINGEMENT OF U.S. PATENT NO.6,351,423**

- 53. United States Patent No. 6,351,423 ("the '423 patent"), entitled "Semiconductor Memory Device Including Sense Amplifier Circuit Differing in Drivability Between Data Write Mode and Data Read Mode," issued on February 26, 2002.
- 54. DRAM MEMTECH is the present assignee of the entire right, title and interest in and to the '423 patent, including all rights to sue for past and present infringement. Accordingly, DRAM MEMTECH has standing to bring this lawsuit for infringement of the '423 patent.
- 55. The various claims of the '423 patent cover, inter alia, DRAM chips comprising internal data write circuitry including a data mask circuit for inhibiting data writing into at least one memory cell in accordance with a data mask designating signal designating the memory cell to be masked of data writing.
- 56. On information and belief, ESMT has been and now is infringing the '423 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips comprising internal data write circuitry including a data mask circuit for inhibiting data writing into at least one memory cell in accordance with a data mask designating signal designating the memory cell to be masked of data writing, including without

limitation, ESMT's M14D5121632A DDR II SDRAM and M13S256328A DDR SDRAM.

- 57. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, ESMT is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '423 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ESMT is and/or has been inducing users to use said chips.
- 58. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, ESMT is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '423 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 59. Accordingly, on information and belief ESMT is liable for infringement of the '423 patent.
- 60. On information and belief, Etron has been and now is infringing the '423 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips comprising

internal data write circuitry including a data mask circuit for inhibiting data writing into at least one memory cell in accordance with a data mask designating signal designating the memory cell to be masked of data writing, including without limitation, Etron's EM68916CWQA DDR II SDRAM and EM6AB160TSA DDR SDRAM.

- 61. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, Etron is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '423 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further Etron is and/or has been inducing users to use said chips.
- 62. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, Etron is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '423 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.
- 63. Accordingly, on information and belief Etron is liable for infringement of the '423 patent.

- 64. On information and belief, ISSI has been and now is infringing the '423 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely DRAM chips comprising internal data write circuitry including a data mask circuit for inhibiting data writing into at least one memory cell in accordance with a data mask designating signal designating the memory cell to be masked of data writing, including, without limitation, ISSI's ISSI IS43DR86400B DDR2 SDRAM and IS43/46LR16800E Mobile DDR SDRAM.
- 65. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, ISSI is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said DRAM chips and/or resellers of said DRAM chips, to infringe the '423 patent, including without limitation by selling, offering for sale and/or importing said DRAM chips; and further ISSI is and/or has been inducing users to use said chips.
- 66. On information and belief, at least since receiving notice of the '423 patent, including at a minimum from this lawsuit, ISSI is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said DRAM chips, at a minimum knowing the same to be especially made or especially

adapted for use in an infringement of the '423 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.

67. Accordingly, on information and belief ISSI is liable for infringement of the '423 patent.

#### **COUNT V**

#### **INFRINGEMENT OF U.S. PATENT NO. 6,028,805**

- 68. United States Patent No. 6,028,805 ("the '805 patent"), entitled "Volatile Memory and Embedded Dynamic Random Access Memory," issued on February 22, 2000.
- 69. DRAM MEMTECH is the present assignee of the entire right, title and interest in and to the '805 patent, including all rights to sue for past and present infringement. Accordingly, DRAM MEMTECH has standing to bring this lawsuit for infringement of the '805 patent.
- 70. The various claims of the '805 patent cover, inter alia, a volatile memory comprising a means for generating a refresh control signal specifying first and second regions of a memory array in a self refresh mode, and a refresh control means for performing self-refresh for said first region and performing no self-refresh for said second region on the basis of the self refresh control signal in a self-refresh mode.
- 71. On information and belief, ESMT has been and now is infringing the '805 patent by actions comprising making, using, selling, offering for sale and/or

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importing synchronous semiconductor memory devices, namely volatile memory comprising a means for generating a refresh control signal specifying first and second regions of a memory array in a self refresh mode, and a refresh control means for performing self-refresh for said first region and performing no self-refresh for said second region on the basis of the self refresh control signal in a self-refresh mode, including without limitation, ESMT's M53D128168A Mobile DDR SDRAM and M52S128168 Mobile SDRAM.

- 72. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, ESMT is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said volatile memory and/or resellers of said volatile memory, to infringe the '805 patent, including without limitation by selling, offering for sale and/or importing said volatile memory; and further ESMT is and/or has been inducing users to use said chips.
- 73. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, ESMT is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said volatile memory, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '805 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.

- 74. Accordingly, on information and belief ESMT is liable for infringement of the '805 patent.
- 75. On information and belief, Etron has been and now is infringing the '805 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely volatile memory comprising a means for generating a refresh control signal specifying first and second regions of a memory array in a self refresh mode, and a refresh control means for performing self-refresh for said first region and performing no self-refresh for said second region on the basis of the self refresh control signal in a self-refresh mode, including without limitation, Etron's EM68916CWQA DDR II SDRAM.
- 76. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, Etron is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said volatile memory and/or resellers of said volatile memory, to infringe the '805 patent, including without limitation by selling, offering for sale and/or importing said volatile memory; and further Etron is and/or has been inducing users to use said chips.
- 77. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, Etron is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or

importing said volatile memory, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '805 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.

- 78. Accordingly, on information and belief Etron is liable for infringement of the '805 patent.
- 79. On information and belief, ISSI has been and now is infringing the '805 patent by actions comprising making, using, selling, offering for sale and/or importing synchronous semiconductor memory devices, namely volatile memory comprising a means for generating a refresh control signal specifying first and second regions of a memory array in a self refresh mode, and a refresh control means for performing self-refresh for said first region and performing no self-refresh for said second region on the basis of the self refresh control signal in a self-refresh mode, including without limitation, ISSI's IS43DR86400B DDR2 SDRAM.
- 80. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, ISSI is and/or has been inducing others, including without limitation manufacturers of apparatuses incorporating said volatile memory and/or resellers of said volatile memory, to infringe the '805 patent, including without limitation by selling, offering for sale and/or importing said volatile memory; and further ISSI is and/or has been inducing users to use said chips.

81. On information and belief, at least since receiving notice of the '805 patent, including at a minimum from this lawsuit, ISSI is and/or has been contributing to the infringement of others, including without limitation said manufacturers, resellers and/or users, including by selling, offering to sell and/or importing said volatile memory, at a minimum knowing the same to be especially made or especially adapted for use in an infringement of the '805 patent, and not a staple article or commodity of commerce suitable for substantial noninfringing use.

- 82. Accordingly, on information and belief ISSI is liable for infringement of the '805 patent.
- 83. As a result of Defendants' infringing conduct, Defendants have damaged DRAM MEMTECH. Defendants are liable to DRAM MEMTECH in an amount that adequately compensates DRAM MEMTECH for their infringement, which, by law, can be no less than a reasonable royalty.
- 84. DRAM Tech intends to seek discovery on the issue of willfulness, and it reserves the right to seek a willfulness finding relative to pre-suit infringement.

  Further, to the extent that any Defendant who was previously unaware of the '838, '831, '747, '423 or '805 patent continues to infringe during the pendency of this suit, such infringement would necessarily be objectively reckless, and thus willful.
- 85. On information and belief, all Defendants have at least had constructive notice of the '838, '831, '747, '423 and '805 patents by operation of law, and DRAM

MEMTECH and any predecessors-in-interest have complied with any marking requirements of 35 U.S.C. § 287 to the extent required by law.

86. As a consequence of these Defendants' infringement, DRAM MEMTECH has been irreparably damaged and such damage will continue without the issuance of an injunction from this Court.

#### **PRAYER FOR RELIEF**

WHEREFORE, DRAM MEMTECH respectfully requests that this Court enter:

- 1. A judgment in favor of DRAM MEMTECH that Defendants have infringed, directly, jointly, and/or indirectly, by way of inducing and/or contributing to the infringement of the '838, '831, '747, '423 and '805 patents.
- 2. A judgment finding that such infringement has been and/or is willful as noted hereinabove, thus entitling DRAM Tech to enhanced damages under 35 U.S.C. § 284;
- 3. A permanent injunction enjoining these Defendants, and their officers, directors, agents, servants, affiliates, employees, divisions, branches, subsidiaries, parents, and all others acting in active concert therewith from infringement, inducing the infringement of, or contributing to the infringement of the '838, '831, '747, '423 and '805 patents.

- 4. A judgment and order requiring the Defendants to pay DRAM MEMTECH its damages, costs, expenses, and prejudgment and post-judgment interest for their respective infringements of the '838, '831, '747, '423 and '805 patents, as provided under 35 U.S.C. § 284;
- 5. A judgment and order finding that this is an exceptional case within the meaning of 35 U.S.C. § 285 and awarding to DRAM MEMTECH its reasonable attorneys' fees; and
- 6. Any and all other relief to which DRAM MEMTECH may show itself to be entitled.

### **DEMAND FOR JURY TRIAL**

Plaintiff, under Rule 38 of the Federal Rules of Civil Procedure, requests a trial by jury of any issues so triable by right.

Respectfully submitted, Dated: February 28, 2011 COLLINS, EDMONDS & POGORZELSKY, PLAC John J. Edmonds Attorney for Plaintiff DRAM MEMORY TECHNOLOGIES LLC 

# UNITED STATES DISTRICT COURT CENTRAL DISTRICT OF CALIFORNIA

#### NOTICE OF ASSIGNMENT TO UNITED STATES MAGISTRATE JUDGE FOR DISCOVERY

This case has been assigned to District Judge David O. Carter and the assigned discovery Magistrate Judge is Suzanne H. Segal.

The case number on all documents filed with the Court should read as follows:

SACV11- 332 DOC (SSx)

Pursuant to General Order 05-07 of the United States District Court for the Central District of California, the Magistrate Judge has been designated to hear discovery related motions.

All discovery related motions should be notice	d on the calendar of th	e Magistrate Judge
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#### **NOTICE TO COUNSEL**

A copy of this notice must be served with the summons and complaint on all defendants (if a removal action is filed, a copy of this notice must be served on all plaintiffs).

Subsequent documents must be filed at the following location:

Western Division 312 N. Spring St., Rm. G-8 Los Angeles, CA 90012	[X] Southern Division 411 West Fourth St., Rm. 1-053 Santa Ana, CA 92701-4516	Eastern Division 3470 Twelfth St., Rm. 134 Riverside, CA 92501
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Failure to file at the proper location will result in your documents being returned to you.

#### UNITED STATES DISTRICT COURT

for the

Central District of California

DRAM MEMORY TECHNOLOGIES LLC	)
Plaintiff	SACV11-00332 DOC(SSX)
v.	) Civil Action No.
ELITE SEMICONDUCTOR MEMORY TECHNOLOGY INC.; ETRON TECHNOLOGY AMERICA, INC; ETRON TECHNOLOGY, INC.; AND INTEGRATED SILICON SOLUTION, INC.  Defendant	) ) )
·	

SUMMONS IN A CIVIL ACTION

To: (Defendant's name and address)

A lawsuit has been filed against you.

Within 21 days after service of this summons on you (not counting the day you received it) — or 60 days if you are the United States or a United States agency, or an officer or employee of the United States described in Fed. R. Civ. P. 12 (a)(2) or (3) — you must serve on the plaintiff an answer to the attached complaint or a motion under Rule 12 of the Federal Rules of Civil Procedure. The answer or motion must be served on the plaintiff or plaintiff's attorney, whose name and address are:

John J. Edmonds COLLINS, EDMONDS & POGORZELSKI, PLLC 1851 East First Street, Suite 900 Santa Ana, California 92705

If you fail to respond, judgment by default will be entered against you for the relief demanded in the complaint. You also must file your answer or motion with the court.

	CLERK OF COURT
MAR - 1 2011	1
Date:	
	Signature of Clerk or Deputy Clerk

## UNITED STATES DISTRICT COURT

for the

Central District of California

DRAM MEMORY TECHNOLOGIES LLC	)	
Plaintiff	)	SACV11-0033200C(SSX)
ν.	)	Civil Action No.
ELITE SEMICONDUCTOR MEMORY TECHNOLOGY INC.; ETRON TECHNOLOGY AMERICA, INC; ETRON TECHNOLOGY, INC.; ANDINTEGRATED SILICON SOLUTION, INC	)	
ELITE SEMICONDUCTOR MEMORY TECHNOLOGY INC.; ETRON TECHNOLOGY AMERICA, INC; ETRON TECHNOLOGY, INC.; AND INTEGRATED SILICON SOLUTION, INC.	) ) )	

#### SUMMONS IN A CIVIL ACTION

To: (Defendant's name and address)

A lawsuit has been filed against you.

Within 21 days after service of this summons on you (not counting the day you received it) — or 60 days if you are the United States or a United States agency, or an officer or employee of the United States described in Fed. R. Civ. P. 12 (a)(2) or (3) — you must serve on the plaintiff an answer to the attached complaint or a motion under Rule 12 of the Federal Rules of Civil Procedure. The answer or motion must be served on the plaintiff or plaintiff's attorney, whose name and address are:

John J. Edmonds COLLINS, EDMONDS & POGORZELSKI, PLLC 1851 East First Street, Suite 900 Santa Ana, California 92705

If you fail to respond, judgment by default will be entered against you for the relief demanded in the complaint. You also must file your answer or motion with the court.

		CLERK OF COURT
Date:	MAR - 1 2011	JULIE PRADO SEAL
		Signature of Clerk or Deputy Clerk

# UNITED STATES DISTRICT COURT, CENTRAL DISTRICT OF CALIFORNIA CIVIL COVER SHEET

I (a) PLAINTIFFS (Check box if you are representing yourself []) DRAM MEMORY TECHNOLOGIES LLC					DI	DEFENDANTS ELITE SEMICONDUCTOR MEMORY TECHNOLOGY INC.; ETRON TECHNOLOGY AMERICA, INC.; ETRON TECHNOLOGY, INC.; AND INTEGRATED SILICON SOLUTION, INC.						
	attomeys (Firm Name, Ad ourself, provide same.)	dress ar	nd Telephone Number, If y	you are	representing At	tomeys	(If Known)					<u> </u>
1	John J. Edmonds, COLLI 1851 East First Street, Suit 1951) 708-1237				rc							
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□ 1 U.	S. Government Plaintiff	<b>a</b> 3	Federal Question (U.S. Government Not a Party	)	Citizen of This Sta	te		TF DEF	Incorporated or lof Business in th		PTF □ 4	DEF □ 4
□ 2 U.	S. Government Defendant	: □4	Diversity (Indicate Citize of Parties in Item III)	enship	Citizen of Another	State	C	32 □2	Incorporated and of Business in A		5	□5
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VI. C	AUSE OF ACTION (Cite	the U.	S. Civil Statute under whi	ch you	are filing and write a	brief st	atement of caus	e. Do not	cite jurisdictional st	atutes unless div	ersity.)	
· T	his action arises under the	patent	laws of the United States,	Title 3	5 of the United State	s Code						
VII. N	ATURE OF SUIT (Plac	e an X i	n one box only.)									
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FOR OFFICE USE ONLY: Case Number: SACV11-00332

AFTER COMPLETING THE FRONT SIDE OF FORM CV-71, COMPLETE THE INFORMATION REQUESTED BELOW.

CV-71 (05/08)

# UNITED STATES DISTRICT COURT, CENTRAL DISTRICT OF CALIFORNIA CIVIL COVER SHEET

VIII(a). IDENTICAL CA If yes, list case number(s):		en previously filed in this court at	nd dismissed, remanded or closed?   No □ Yes	
VIII(b). RELATED CAS If yes, list case number(s):		en previously filed in this court the	at are related to the present case? ☑No ☐ Yes	
	☐ A. Arise from the ☐ B. Call for determ ☐ C. For other reaso	ons would entail substantial duplic	ons, happenings, or events; or Ily related or similar questions of law and fact; or cation of labor if heard by different judges; or t, and one of the factors identified above in a, b or c also is present.	
IX. VENUE: (When comp	leting the following info	ormation, use an additional sheet i	if necessary.)	
			if other than California; or Foreign Country, in which EACH named plaintiff resides.  f this box is checked, go to item (b).	
County in this District:*			California County outside of this District; State, if other than California; or Foreign Country	
Orange County				
			if other than California, or Foreign Country, in which <b>EACH</b> named defendant resides.  If this box is checked, go to item (c).	
County in this District:*			California County outside of this District; State, if other than California; or Foreign Country	
			Santa Clara County (Etron Technology America, Inc. and Integrated Silicon Solution, Inc.); Taiwan (Elite Semiconductor Memory Technology Inc and Etron Technology, Inc.)	
A CONTRACTOR OF THE PROPERTY O	and the same of	nty outside of this District; State in ocation of the tract of land involved	if other than California; or Foreign Country, in which EACH claim arose.	
County in this District:*			California County outside of this District; State, if other than California; or Foreign Country	
Claim I-V : Los Angeles	County			
		de, Ventura, Santa Barbara, or S	11	
X. SIGNATURE OF ATTO	RNEY (OR PRO PER)		Date Februs, -, 28,2011	
or other papers as requi	red by law. This form, a	pproved by the Judicial Conference	rmation contained herein neither replace nor supplement the filing and service of pleadings the of the United States in September 1974, is required pursuant to Local Rule 3-1 is not filed ting the civil docket sheet. (For more detailed instructions, see separate instructions sheet.)	
Key to Statistical codes relat	ing to Social Security C	ases:		
Nature of Su	it Code Abbreviatio	on Substantive Statement of	f Cause of Action	
861	HIA		rance benefits (Medicare) under Title 18, Part A, of the Social Security Act, as amended. ospitals, skilled nursing facilities, etc., for certification as providers of services under the SFF(b))	
862	BL	All claims for "Black Lung (30 U.S.C. 923)	g" benefits under Title 4, Part B, of the Federal Coal Mine Health and Safety Act of 1969.	
863	DIWC		d workers for disability insurance benefits under Title 2 of the Social Security Act, as filed for child's insurance benefits based on disability. (42 U.S.C. 405(g))	
863	DIWW	All claims filed for widow Act, as amended. (42 U.S.	s or widowers insurance benefits based on disability under Title 2 of the Social Security (.C. 405(g))	
864	SSID	All claims for supplementa Act, as amended.	al security income payments based upon disability filed under Title 16 of the Social Security	
865	865 RSI All claims for retirement (old age) and survivors benefits under Title 2 of the Social Security Act, as amended. (42			